

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5	first adj3 processor and second adj3 processor and (switch or selector) with processor and instruction translator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:48
S2	30	first adj3 processor and second adj3 processor and (switch or selector) with processor and translat\$3 and (arm and mips)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/25 16:57
S3	25	first adj3 processor and second adj3 processor and (switch or selector) with processor and instruction with translat\$3 and (arm and mips)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/25 17:34
S7	2	wireless adj tranceiver and cell phone	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/25 17:39
S8	4574	bluetooth and cell phone	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/25 17:39
S9	8	bluetooth and cell phone and tranceiver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/25 17:39
S13	63	(plural\$5 or multip\$5 or many or two) adj3 processor and instruction (translat\$4 or interpret\$3 or conver\$4) and (MIPS and RISC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 10:55

EAST Search History

S14	32	(plural\$5 or multip\$5 or many or two) adj3 processor and instruction (translat\$4 or interpret\$3 or conver\$4) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:02
S15	47	(plural\$5 or multip\$5 or many or two) adj3 processor and (processor or instruction) (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:12
S16	25	ARM adj3 processor and MIPS adj3 processor and (processor or instruction) (selector or switch or multiplex\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:07
S17	106	(plural\$5 or multip\$5 or many or two or more) adj3 processor and (processor or instruction) adj3 (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 18:34
S18	57	(plural\$5 or multip\$5 or many or two or more) adj3 processor and (processor or instruction or mode) adj (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:21
S19	1	(plural\$5 or multip\$5 or many or two or more) adj3 processor and (processor or instruction or mode) adj (selector or switch or multiplex\$3) and (MIPS and ARM) and 712/34.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:39
S21	65	("5638525").URPN.	USPAT	NEAR	ON	2007/01/26 11:34
S22	2	("4839797" "5481684").PN.	US-PGPUB; USPAT; USOCR	NEAR	ON	2007/01/26 11:37

EAST Search History

S24	3	different near3 processor near3 (architec\$4 or type or famil\$4) and (processor or instruction or mode) adj (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 11:46
S25	35	different near3 processor near3 (architec\$4 or type or famil\$4) and (processor or instruction or mode) adj (selector or switch or multiplex\$3) and (MIPS or ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:52
S26	82	different near3 processor near3 (architec\$4 or type or famil\$4) and first adj3 processor and second adj3 (processor) and (MIPS or ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 12:09
S27	55	different near3 processor near3 (architec\$4 or type or famil\$4) and first adj3 processor and second adj3 (processor) and (MIPS or ARM) and (processor or instruction or mode) with (select\$3 or switch\$3 or multiplex\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 12:10
S28	429	(radio or RF) (detect\$3 or sniff\$4) and (Bluetooth or \$4"802.11"\$1) and (tranceiver or (transmitter and receiver))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 14:02
S30	83	(radio or RF) (detect\$3 or sniff\$4) and (Bluetooth or \$4"802.11"\$1) and (tranceiver or (transmitter and receiver)) and (digital with analog) and digital near3 processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 14:11
S31	47	(radio or RF) (detect\$3 or sniff\$4) and (Bluetooth or \$4"802.11"\$1) and (tranceiver or (transmitter and receiver)) and (digital with analog) and digital near3 processor and short\$1range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 14:11

EAST Search History

S32	34	dual\$1core processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 16:07
S33	28	(multi\$1core or dual\$1core) processor with integrated circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 16:07
S36	5	(plural\$5 or multip\$5 or many or two or more) adj3 processor and (processor or instruction) adj3 (selector or switch or multiplex\$3) and (MIPS and ARM) and "710"/\$. ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/26 18:34
S37	284	(soc or system\$1on\$1chip or system on chip) and (dual processor or (first processor and second processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 10:14
S38	19	(soc or system\$1on\$1chip or system on chip) same (dual processor or (first processor and second processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:18
S41	5	(soc or system\$1on\$1chip or system on chip) same (dual processor or (first processor and second processor)) and ((processor with programmable) or \$1"PGA")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:47
S42	5	2002/0128037	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:43

EAST Search History

S43	2	"20020128037".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:43
S44	9378	(dual (core or processor) or (first processor and second processor)) and (processor with (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:49
S45	340	(dual (core or processor) or (first processor and second processor)) same (ic or integrated circuit) and (processor with (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:50
S46	345	(dual (core or processor) or (first processor and second processor)) same (ic or integrated circuit or SOC) and (processor with (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:50
S47	201	(dual (core or processor) or (first (core or processor) and second processor)) with (ic or integrated circuit or SOC) and (processor with (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:56
S48	170	(dual (core or processor) or (first (core or processor) and second processor)) with (ic or integrated circuit or SOC) and (processor near3 (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 12:58
S49	170	(dual (core or processor) or (first (core or processor) and second processor)) with (ic or integrated circuit or SOC or system\$1on\$1chip or system on chip) and (processor near3 (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 13:24

EAST Search History

S50	29	(dual (core or processor) or (first (core or processor) and second processor)) with ((common or single or "same") integrated circuit or SOC or system\$1on\$1chip or system on chip) and (processor near3 (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 13:12
S51	22	(dual (core or processor) or (first (core or processor) and second processor)) with ((common or single or "same") integrated circuit or SOC or system\$1on\$1chip or system on chip) and (processor near (program\$4 or configur\$6))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 13:14
S53	23	(dual (core or processor) or (first (core or processor) and second processor)) with ((common or single or "same") integrated circuit or SOC or system\$1on\$1chip or system on chip) and ((program\$4 or configur\$6) adj3 processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 13:23
S54	2454	processor core with (program\$5 or \$2configur\$6)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 13:24
S55	36	(dual (core or processor) or (first (core or processor) and second processor)) with (ic or integrated circuit or SOC or system\$1on\$1chip or system on chip) and processor core with (program\$5 or \$2configur\$6)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:48
S56	434	"710"/\$.ccls. and first adj3 processor and second adj3 processor and (switch or selector) with processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:50
S57	7	"710"/58.ccls. and first adj3 processor and second adj3 processor and (switch or selector) with processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:51

EAST Search History

S58	11	"710"/104.ccls. and first adj3 processor and second adj3 processor and (switch or selector) with processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2007/01/31 16:51
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